Docket No. TRANSMITTAL OF FORMAL DRAWINGS 15184 In Re Application Of: COPY OF PAPERS SEP INDO ORIGINALLY FILED Serial No Filing Date Batch No. **Art Unit** Examiner 10/028,111 Inber 20, 2001 unassigned unassigned unassigned Invention: WATCHDOG TIMER AND METHOD FOR DETECTING ABNORMAL OPERATION OF COMPUTER, AND COMPUTER INCLUDING THE TIMER Address to: **Assistant Commissioner for Patents** RECEIVED Washington, D.C. 20231 MAR 0 6 2002 **Technology Center 2100** Transmitted herewith are: 10 sheets of formal drawing(s) for this application. Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c) on the reverse side of the drawing. Dated: January 24, 2002 Signature

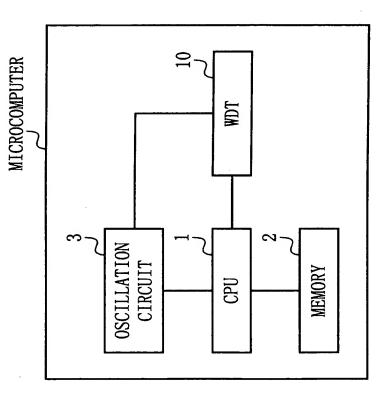
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I certify that this document and attached formal drawings are being deposited on Jan 24, 2002 with the U.S. Postal Service as first class mall under 37 C.F.R. 1.8 and addressed to the Assistant Compressioner for Patents, Washington D.C. 20231.

Signature of Person Mailing Correspondence

Michelle Mustafa

Typed or Printed Name of Person Mailing Correspondence



FTG. 1

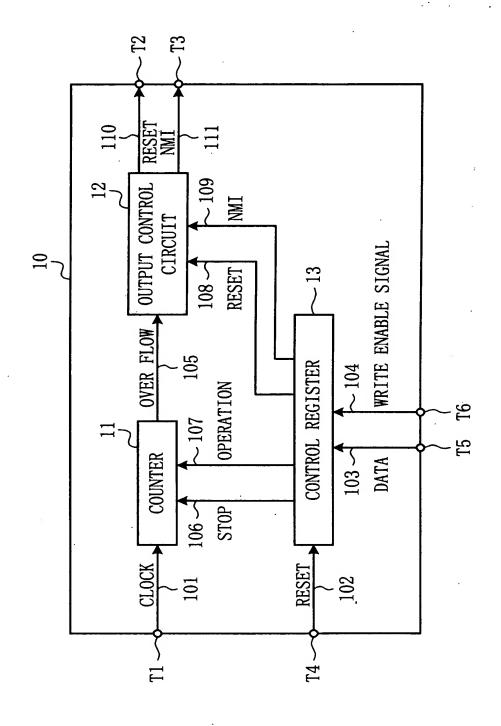
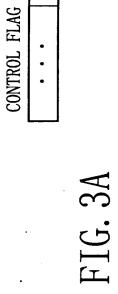
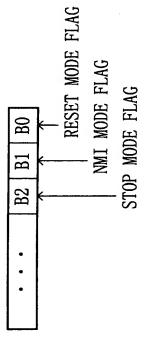


FIG. 2





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OPERATIONAL MODE	B2	BI	B0
RESET MODE	×	×	0
NMI MODE	×	0	1
STOP MODE	0	Ţ	1
)(I: X	N'T	X:DON'T CARE	(*)

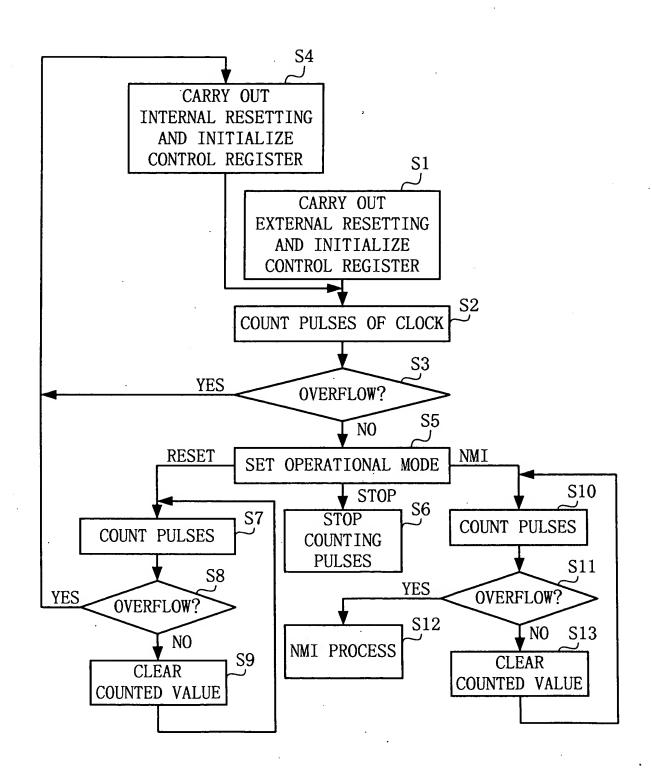
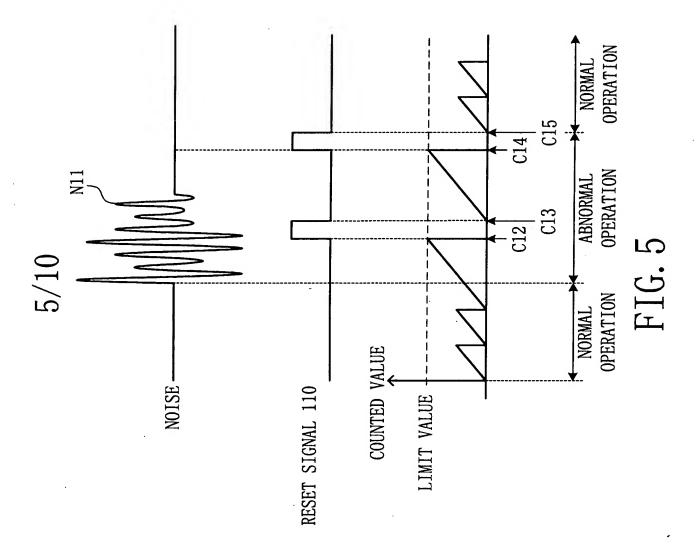
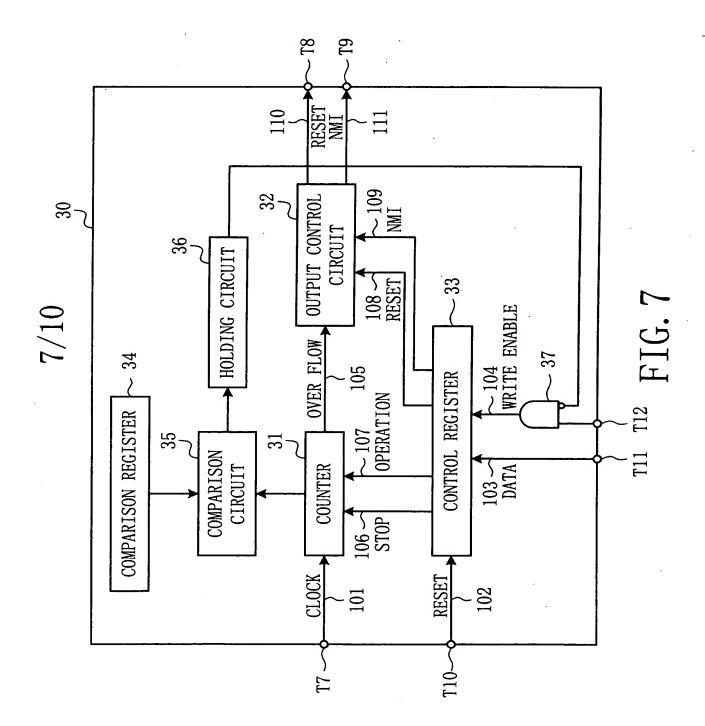


FIG. 4





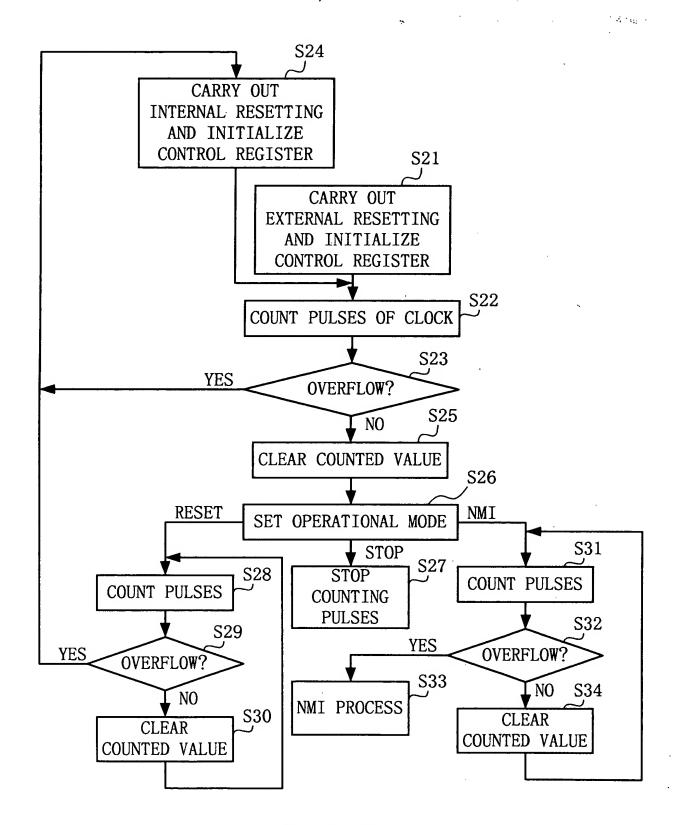


FIG. 8

	ABNORMAL OPERATION	FACTOR
A	• TRANSISTOR(Tr) DOES NOT OPERATE • CLOCK FOR WDT IS NOT PROVIDED	• BREAKAGE OF POWER SOURCE LINE • BREAKAGE OF OSCILLATOR
В	• Tr OPERATES • CLOCK FOR WDT IS PROVIDED • SOFTWARE DOES NOT RUN	• ABNORMAL OPERATION IN CPU, ROM, RAM, ETC. (SUCH AS VOLTAGE DROP IN CPU)
၁	• Tr OPERATES • CLOCK FOR WDT IS PROVIDED • SOFTWARE RUNS • TEMPORARY ABNORMALITY	• COMMON NOISE (E. G. IN POWER SOURCE) • ERROR IN SOFTWARE DESIGN (E. G. INDEFINED CONDITION)

FIG. 9

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